

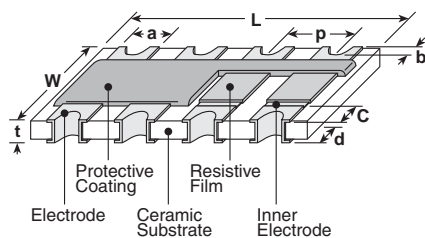
## concave termination with square corners resistor array



### features

- Manufactured to type RK73 standards
- Less board space than individual chips
- Isolated resistor elements
- Marking: Marked with resistance value
- Products with lead-free terminations meet EU RoHS requirements. Pb located in glass material, electrode and resistor element is exempt per Annex 1, exemption 5 of EU directive 2005/95/EC

### dimensions and construction



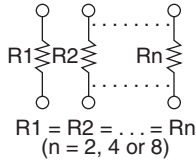
Size Code	Dimensions inches (mm)								
	L	W	C	d	t	a (top)	a (bot.)	b	p (ref.)
1J2	.063±.006 (1.6±0.15)								
1J4	.126±.008 (3.2±0.2)	.063±.006 (1.6±0.15)	.012±.006 (0.3±0.15)	.016±.006 (0.4±0.15)		.020±.006 (0.5±0.15)	.016±.006 (0.4±0.15)		.031 (0.8)
1J8	.252±.008 (6.4±0.2)								
2A2	0.1±.008 (2.54±0.2)								
2A4	0.2±.008 (5.08±0.2)	.079±.008 (2.0±0.2)	.016±.008 (0.4±0.2)		.024±.004 (0.6±0.1)			.006 (0.15)	
2A8	0.4±.008 (10.16±0.2)								
2B2	0.1±.008 (2.54±0.2)			.022±.006 (0.55±0.15)		.031 (0.8)	.030±.006 (0.75±0.15)		.050 (1.27)
2B4	0.2±.008 (5.08±0.2)	.126±.008 (3.2±0.2)	.020±.012 (0.5±0.3)						
2B8	0.4±.008 (10.16±0.2)								

### ordering information

New Part #	CN	1J	4	T	TD	101	J
Type				Termination Material	Packaging	Nominal Resistance	Tolerance
		1J 2A 2B	2 4 8	T: Sn (Other termination styles may be available, please contact factory for options)	TE: 7" embossed plastic TD: 7" paper tape TED: 10" embossed plastic TDD: 10" paper tape	2 significant figures + 1 multiplier for ±2 & ±5% 3 significant figures + 1 multiplier for ±1%	F: ±1% G: ±2% J: ±5%

For further information on packaging, please refer to Appendix A.

### circuit schematic

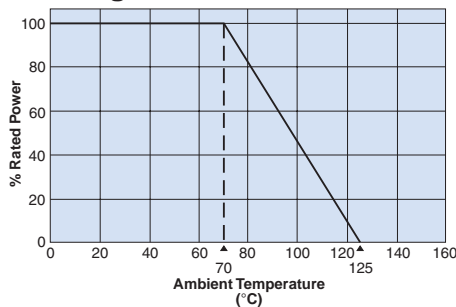


### applications and ratings

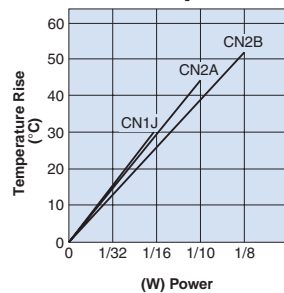
Part Designation	Power Rating @ 70°C (Per Element)	T.C.R. (ppm/°C) Max.	Resistance Range (Ω)			Absolute Maximum Working Voltage	Maximum Overload Voltage (5 Secs. Max.)	Operating Temperature Range
			E-24, E-96 (F:±1%)	E-24 (G:±2%)	E-24 (J:±5%)			
CN1J2	1/16W (.063W)	±200: R≥10Ω	10 - 1M	10 - 1M	10 - 1M	50V	100V	-55°C to +125°C
CN1J4					1 - 1M			
CN1J8					10 - 1M			
CN2A2	1/10W (.100W)	±400: R<10Ω	10 - 1M	10 - 1M	10 - 1M	100V	200V	
CN2A4					—			
CN2A8	1/8W (.125W)	±400: R<10Ω	10 - 1M	10 - 1M	10 - 1M	200V	400V	
CN2B2					—			
CN2B4					—			
CN2B8					—			

### environmental applications

#### Derating Curve



#### Surface Temperature Rise



### Performance Characteristics

Parameter	Maximum Δ R	Test Method
Thermal Shock	±(1.0% + 0.1Ω)	MIL-STD-202, Method 107, -55°C to +125°C, 5 cycles
Low Temperature Operation		MIL-R-55342 π 4.7.4, 1 hour @ -55°C followed by 45 minutes of RCWV*
High Temperature Exposure		MIL-R-55342 π 4.7.6, 100 hours @ 125°C
Short Time Overload	±(2.0% + 0.05Ω)	MIL-R-55342 π 4.7.5, 2.5 x RCWV for 5 seconds
Resistance to Solder Heat	±(1.0% + 0.1Ω)	MIL-R-55342 π 4.7.7, 260°C for 10 seconds
Terminal Strength-Push		1.2 Kg for 1 minute
Terminal Strength-Bend	±(0.5% + 0.05Ω)	5mm deflection in either direction for 10 seconds
Moisture Resistance	±5.0%	MIL-STD-202, Method 103, 40°C, 90 - 95% RH, 1000 hours
Life		MIL-STD-202, Method 108, 70°C, 1000 hours @ RCWV, 1.5 hr ON, 0.5 hr OFF
Pulse		2.5 x RCWV, not exceeding max. overload voltage, 1 sec. ON, 25 sec. OFF, 10,000 cycles
Temperature Cycling	±1.0%	30 min. @ -55°C, 15 min. @ +25°C, 30 min. @ +125°C, 15 min. @ +25°C, 5 cycles
Terminal Adhesion	15 Grams Minimum	Axial pull, one terminal at a time
Dielectric Withstanding Voltage CN1J, CN2A, CN2B	100V, 400V, 400V	1 minute minimum MIL-STD-202, Method 301
Insulation Resistance	1,000 MΩ Minimum	—

\* RCWV = Rated Continuous Working Voltage.